

Calculate Linux - Bug # 275: using smp_processor_id() in preemptible [00000000] code: X/6937

Status:	Feedback	Priority:	Normal
Author:	Ð~Ð²Ð°Ð½•ÐšÐ°Ñ•ÑŒÑ•Ð½Ð¾Ð²	Category:	
Created:	04/22/2011	Assignee:	
Updated:	05/21/2013	Due date:	
Subject:	using smp_processor_id() in preemptible [00000000] code: X/6937		
Description:	Ð' syslog Ð²ÑÐ²Ð¾Ð´Ð½,ÑÑÑ @BUG: using smp_processor_id() in preemptible [00000000] code: X/6937 caller is KAS_GetExecutionLevel+0x15/0x180 [fglrx] Pid: 6937, comm: X Tainted: P W 2.6.38.3-calculate #1 Call Trace: [<ffffff811a2fb5>] ? debug_smp_processor_id+0xd5/0xf0 [<ffffffa0526db5>] ? KAS_GetExecutionLevel+0x15/0x180 [fglrx] [<ffffffa0541c59>] ? MCIL_GetExecutionLevel+0x39/0x80 [fglrx] [<ffffffa05f108b>] ? CallbackQueueAccess+0x2b/0x2a0 [fglrx] [<ffffffa05e5b20>] ? UnRegisterIRQClient_Worker+0x0/0xc0 [fglrx] [<ffffffa05417b2>] ? MCIL_bMiniportCapEnabled+0x82/0xa0 [fglrx] [<ffffffa05e5c63>] ? UnRegisterIRQClient+0x83/0xe0 [fglrx] [<ffffffa05e2dfe>] ? IRQMGR_IRQSourceSupported+0x7e/0xa0 [fglrx] [<ffffffa05e2f51>] ? IRQMGR_Access+0x131/0x190 [fglrx] [<ffffffa0558043>] ? fireglAsyncioUnregisterIntMsgHandlers+0x303/0x400 [fglrx] [<ffffff8137edd2>] ? _raw_spin_unlock_irqrestore+0x12/0x40 [<ffffffa058edb4>] ? asyncIONotifyMsg+0x254/0x420 [fglrx] [<ffffffa05571c3>] ? firegl_asyncio_write+0x193/0x250 [fglrx] [<ffffffa05298bb>] ? ip_firegl_write+0x5b/0xa0 [fglrx] [<ffffff810de538>] ? vfs_write+0xc8/0x180 [<ffffff810de6dc>] ? sys_write+0x4c/0x90 [<ffffff81002d3b>] ? system_call_fastpath+0x16/0x1b@		

History

05/18/2011 01:01 pm - Mikhail Hiretsky

- Status changed from New to Feedback

$\mathcal{D}'\mathcal{D}^{\frac{3}{4}}\mathcal{D}^2\tilde{\mathcal{N}}\in\mathcal{D}\mu\mathcal{D}^{\frac{1}{4}}\tilde{\mathcal{N}}\cdot\tilde{\mathcal{N}}\pm\mathcal{D}\mu\mathcal{D}^3\mathcal{D}^{\frac{3}{4}}\mathcal{D}^2\mathcal{D}^{\frac{3}{4}}\mathcal{D}\cdot\mathcal{D}^{\frac{1}{2}}\mathcal{D},\mathcal{D}^0\mathcal{D}^0\mathcal{D}\mu\tilde{\mathcal{N}},\mathcal{D}'\mathcal{D}^0\mathcal{D}^{\frac{1}{2}}\mathcal{D}^{\frac{1}{2}}\mathcal{D}^0\tilde{\mathcal{N}}\cdot\mathcal{D}^{\frac{3}{4}}\tilde{\mathcal{N}}'\mathcal{D},\mathcal{D}\pm\mathcal{D}^0\mathcal{D}^0?$

05/22/2011 10:10 am - $\mathbb{D}^{\sim}\mathbb{D}^2\mathbb{D}^{\circ}\mathbb{D}^1_{1/2}$ $\mathbb{D}\mathbb{S}\mathbb{D}^{\circ}\tilde{\mathbb{N}}\cdot\tilde{\mathbb{N}}\mathbb{C}\tilde{\mathbb{N}}\cdot\mathbb{D}^{1/2}\mathbb{D}^{3/4}\mathbb{D}^2$

Mike Hiretsky wrote:

$$> \mathfrak{D}'\mathfrak{D}^{3/4} \mathfrak{D}^2\tilde{\mathfrak{N}}\in\mathfrak{D}\mu\mathfrak{D}^{1/4}\tilde{\mathfrak{N}}\cdot \tilde{\mathfrak{N}}\nmid\mathfrak{D}\mu\mathfrak{D}^3\mathfrak{D}^{3/4} \mathfrak{D}^2\mathfrak{D}^{3/4}\mathfrak{D}\cdot\mathfrak{D}^{1/2}\mathfrak{D}, \mathfrak{D}^0\mathfrak{D}^{\circ}\mathfrak{D}\mu\tilde{\mathfrak{N}}, \mathfrak{D}'\mathfrak{D}^{\circ}\mathfrak{D}^{1/2}\mathfrak{D}^{1/2}\mathfrak{D}^{\circ}\tilde{\mathfrak{N}}\cdot \mathfrak{D}^{3/4}\tilde{\mathfrak{N}}^{\circ}\mathfrak{D}, \mathfrak{D}\pm\mathfrak{D}^0\mathfrak{D}^{\circ}?$$
$$\tilde{N} \dots \mathcal{D}^{1/4} \tilde{N} \cdot \mathcal{D} \gg \mathcal{D}^{3/4} \mathcal{D} \nmid \mathcal{D}^{1/2} \mathcal{D}^{3/4} \mathcal{D}^{3/4} \tilde{N}, \tilde{N} \cdot \mathcal{D} \gg \mathcal{D} \mu \mathcal{D}' \mathcal{D} \cdot \tilde{N}, \tilde{N} \in \cdot \mathcal{D} \zeta, \mathcal{D}^{3/4} \tilde{N} \cdot \mathcal{D}^{3/4} \mathcal{D}^2 \mathcal{D} \mu \tilde{N}, \tilde{N} f \mathcal{D}^1 \tilde{N}, \mathcal{D} \mu \mathcal{D}^0 \mathcal{D}^0 \mathcal{D}^0 \tilde{N} \cdot \tilde{N}, \mathcal{D}^{3/4} \tilde{N} \cdot \mathcal{D}' \mathcal{D} \mu \mathcal{D} \gg \mathcal{D}^0 \tilde{N}, \tilde{N} \in \cdot$$

05/21/2013 01:47 pm - Alexander Nein

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<http://forums.gentoo.org/viewtopic-t-843415.html>

N̈ĖĐμĐ°Đ¼Đ½ĐμĐ½Đ̣ N̄fṄŽŃ, Đ¾N,NĐ°Đ»N̂ŽN̄±Đ,N̄N̄Đ Đ² Ñ•Đ'ÑĖĐμ Đ¾ĐĚN̄+Đ,N̂ CONFIG_DEBUG_PREEMPT, Ñ±N̄,Đ¾ Đ±N̄, Đ½Đμ Đ·Đ°N̄•Đ¾N̄ĖN̄.N̄N̄Đ Đ»Đ¾Đ°Đ.